Stack and Program Counter

From MPU Buffered Address AB0 thru AB12

Vector-Generator Address Selector

Vector-Generator RAM

Vector-Generator Data Buffer

NOTE:
Either a 74LS245 or an AM8304B may be used at location H3. Pin numbers not enclosed in parentheses are for 74LS245. Pin numbers in parentheses are for an AM8304B.

Vector-Generator Data Shifter

To Vector Timer Control and Vector-Generator X-Y Output

To Stack Input and Vector-Generator Output

To Vector Timer Control, Vector Timer, State Machine and Stack

To Vector-Generator Color Output

To Vector Timer Control and Vector-Generator Output

Vector-Generator ROM

NOTE
- Indicates edge connector (520)
- Indicates interconnect connector (519)
- Indicates test point

State Machine Control

Vector Timer Control

To Vector Generator Memory Shifter

Vector Timer

State Machine

To Vector Timer Control and Stack
+
To Vector-Generator Memory Shifter

To Vector-Generator Output

Vector Halt Latch

From Reset Counter

From Address Decoder

From Address Decoder

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Sheet 3, Side A
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Vector-Generator Program Counter
Vector-Generator RAM
Vector-Generator ROM
Vector-Generator Data Shifter
Vector-Generator Data Buffer
Vector-Generator Address Selector
Vector-Generator Vector Timer
Vector-Generator State Machine
Section of 037383-01 & -02 B

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DP-190-03 1st printing
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