Z8002 INTERFACE POD RAM TEST

FOR POLE POSITION

The Z8002 Interface Pod tests RAM by checking upper and lower bits during the same test.

For example:

RAM ADDRESS	8000	Your loading Lower data D0-D7
RAM ADDRESS	8001	Your loading Upper data D0-D15
RAM ADDRESS	8002	Your loading Lower data D0-D7
RAM ADDRESS	8003	Your loading Upper data D8-D15

Every other address line you switch which data line you control.

EVEN NUMBERS LOWER DATA D0-D7
ODD NUMBERS UPPER DATA D8-D15

If you do a RAM test starting at 8000. And the compare error LED lights at address 83F1. Your problem exists in the upper data area.

8000-8FFF
MOTION OBJECT MEMORY
ON VIDEO PCB
SHEET 13B

9000-97FF
ROAD MEMORY
VIDEO PCB
SHEET 12A

9800-9FFF
ALPHA NUMERICS
SHEET 12A

R/W MODE TO PULSE, THEN OFF.
IF GOOD: THEN ADDRESS DISPLAY WILL READ 8000.(GO TO NEXT STEP)
IF BAD: THEN COMPARE ERROR LED WILL LIGHT.(SEE TROUBLE SHOOTING RAM TEST)

CHECKSUM TEST

To test the ROM, you must remove the Z8002 (DUT) from its socket. Then place the interface's harness into the MPU socket. Ground the RESET test point. (RESET line of the sound MPU.)

THE CAT BOX SWITCH SETTINGS:

R/W MODE ----------= (OFF)
R/W ----------= READ
ERROR DATA DISPLAY ----= GAME
BYTES ----------= 1024
DBUS SOURCE ----------= ADDR
** CHECKSUM ----------= (ON)
TESTER MODE ----------= R/W

Z8002 INTERFACE SETTINGS

RAM/ROM ----------= ROM
LBYTE ----------= D0-D7
HBYTE ----------= D8-D15

MPU A

HBYTE ADDRESS	ROM	CHECKSUM
0000	-104,4E	18A4
2000	-115,4D	NOT USED

LBYTE ADDRESS	ROM	CHECKSUM
0000	-103,3E	343E
2000	114,3D	NOT USED

MPUB

HBYTE ADDRESS	ROM	CHECKSUM
0000	-102,4L	0132
2000	-115,4H	NOT USED

LBYTE ADDRESS	ROM	CHECKSUM
0000	-101,3L	AB86
2000	-112,3H	NOT USED